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SEP 1 7 2004

In re Patent Application of:	Confirmation No.: 8820
Talledo, Cesar A. et al.	) ) Group Art Unit: 2816
Applic.Serial No.: 10/621,055	)
,	Examiner: Cox, Cassandra F.
Filed: 07/15/2003	
For: CLOCK SIGNAL PROCESSING	) }
LOGIC AND METHOD FOR	
DETERMINING CLOCK SIGNAL	·
CHARACTERISTICS IN	)
REFERENCE VOLTAGE AND	)
TEMPERATURE VARYING	)
ENVIRONMENTS	
	Fremont, California

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 703-872-9306) on the date indicated below.

siche H. Ocumb 9-17-2004
gnature Date

## ELECTION/RESTRICTIONS AMENDMENT

Sir:

This communication is in response to the Notice of Non-Compliant Amendment mailed September 13, 2004, and replaces the non-compliant communication filed on August 31, 2004 in response to the Office Action mailed August 25, 2004.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 20 of this paper.

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Atty. Docket No. IDTI.002US1-1826

September 17, 2004